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Advanced Digital Logic Design - EECS 303M. Morris Mano And Charles R. Kime. Logic And Computer Design Fundamentals. Prentice-Hall, NJ, Fourth Edition, 2008 7 Robert Dick Advanced Digital Logic Design. ... 3 Understand How To Use Schematic Capture Software To Design Digital Circuits. 4 Be Capable Of Doing Automa Mar 14th, 2024 Advanced Digital Logic Design - EECS 303 Today's Goals Benjamin/Cummings Publishing Company, Inc., 1994 8 Robert Dick Advanced Digital Logic Design Administration Overview Of Course Homework Misc. Grading Policies Homeworks: 25% Of Grade Labs: 25% Of Grade Midterm Exam: 20% Of Grade Final Exam: 30% Of Grade Homeworks And Labs Due At Beginning Of Class On Due Date Jan 8th, 2024 Syllabus For CSCE 611: Advanced Digital Design (rev. 10/18/21) Section 001 M 3:55-5:10 In Swearingen 2A31 W 3:55 To 5:10 In Swearingen 3D22 Section 002 M 3:55-5:10 In Swearingen 2A31 Th 3:55 To 5:10 In Swearingen 3D22 3D22 Combination: 5-1-2-4-3 3 Internet Resources 1. <https://dropbox.cse.sc.edu> : Handouts, Reference Material, Announcements, Lecture Slides, Project Submis- Jan 8th, 2024.

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