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 Current Source). R_V V_{DD} GND M_0 M_2 M_1 M_3 GND 4 GND I_{Out} Assume All
 Transistors Are Operating In Saturation Region And $\beta_n = \beta_p = 1$, $V_{DD} = 3$ V, $V_{TH}(NMOS)$
 $= 0$... May 4th, 2024EE 338L: Analog Integrated Circuit Design Fall 2012Output
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 Razavi, Fundamental Of Microelectronics, Wiley, 2008. Other Helpful References:
 Paul Gray Et Al., Analysis And Design Of Analog Integrated Circuits, Wiley, 2009.
 Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw-Hill, 2000. Mar
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