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230 Words With Byte Addresses 0, 4, 8, ... 2<sup>32</sup> -4 Words Are Aligned, That Is, Each Has An Address That Is A Multiple Of 4. MIPS Can Be Either Big-endian (that Is, The Address Of Each Word Is The Address Of The “left-most” Byte Of The Word) Or Little-endian Feb 23th, 2024

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