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Reference Verilog Reference Guide V About This

Manual This Manual Describes How To Use The Xilinx

Foundation Express Program To Translate And

Optimize A Verilog HDL Description Into An Internal

Gate-level Equivalent. Before Using This Manual, You

Should Be Familiar With The Operations That Are

Common To All Xilinx Software Tools. These Operations

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Design Examples The Next Pages Contain The Verilog

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