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NAND GATE: The NAND Gate Is A Contraction Of AND-
NOT. The Output Is High When Both Inputs Are Low
And Any One Of The Input Is Low .The Output Is Low

Level When Both Inputs Are High. NOR GATE: SL No.
COMPONENT SPECIFICATION QTY 1. AND GATE IC 7408
1 2. OR GATE IC 7432 1 3. NOT GATE IC 7404 1 4.
NAND GATE 2 I/P IC 7400 1 5. NOR GATE IC 7402 1
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AND Gate: The AND Operation Is Defined As The
Output As One If And Only If All The Inputs Are One.
7408 Is The Two Input AND Gate IC. A & B Are The Input
Terminals & Y Is The Output Terminal. $Y = A.B$ 7th,
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8: Biotechnology: Bacterial Transformation Lab 9:
Biotechnology: Restriction Enzyme Analysis Of DNA
Lab 10: Energy Dynamics Lab 11 ... 11th, 2024

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Final Zeros Are Significant If There Is A Decimal
Present. For Example 200kg. And 2,500kg. Have Three
And Four Significant Figures, Respectively. 6. Final
Zeros After A Decimal Are Significant. For Example,
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