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# **EEDG/CE 6303: Testing And Testable Design**

Other References: VLSI Test Principles And Architectures, L-T Wang, C-W Wu And X. Wen, Morgan Kaufmann, 2006. Feb 4th, 2024

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The Conservative Logic Gates Are Feynman, Toffoli, And Fredkin. The Design Of Two Vectors Testable Sequential Circuits Based On Conservative Logic Gates. All Sequential Circuit Based On Conservative Logic Gates Can Be Tested For Classical Unidirectional Stuck-at Faults May 7th, 2024

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1.9.Show That The Set Of Locally Integrable Signals L Loc, The Set Of Absolutely Integrable Signals L1, And The Set Of Square Integrable Signals L2 Are Linear Shift-invariant Spaces. Solution: Let X;y2L1 And A;b2C. Now Kax+ Byk  $1 = Z \ 1 \ Jax(t) + By(t)jdt \ Z \ 1 \ 1 \ Ajx(t)j+ Bjy(t)jdt Triangle Inequ Feb 2th, 2024$ 

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Proceedings Of The 57th Annual Meeting Of The Association For Computational Linguistics , Pages 747 763 Florence, Italy, July 28 - August 2, 2019. Jan 3th, 2024

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