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15.00 \$ 4.00 \$ 26.00 \$ 2.00 \$ 6.00 \$ 19.00 \$ 45.00 \$ 15.00 \$ 2.00 \$ 10.00 \$ 13.00 Jan 4th, 2024.

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MC100EP016A 3.3 V ECL 8-Bit Synchronous Binary Up Counter3.3 V ECL 8-Bit Synchronous Binary Up Counter Description The MC100EP016A Is A High-speed Synchronous, Presettable, Cascadeable 8-bit Binary Counter. Architecture And Operation Are The Same As The ECLinPS™ Family MC100E016 With Higher Operating Speed. The Counter Features Internal Feedback To TC Gated By The TCLD (Terminal Count Load) Pin. Feb 12th, 2024Pierce™ ECL Western Blotting SubstrateSolution Per Cm 2 Of Membrane. Note: For Best Results Prepare Working

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