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In Addition, A Ferrite Bead Should Be Used To Isolate Each Analog Supply From The Rest Of The Board. The Bead Should Be Placed In Series ... Steward Hl 1206N101R-00 Or Similar. Note:Place Vias Close To Or Under Neath The Capacitor Pads For Low Inductive Path And Higher Heat Dissipation Rate. 0.1 UF. A B C Mar 2th, 2024

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Dimension Is Based On An Equivalent Surface Contact Area. Table 18-1. Lattice SMD/NSMD Pad Recommendations 1 0.35 Mm ... Apr 1th, 2024

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