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PHY Interface For The PCI Express* Architecture PCI Express 3PHY Functions Which Must Be Incorporated In A PIPE Compliant PHY, And It Defines A Standard Interface Between Such A PHY And A Media Access Layer (MAC) & Link Layer ASIC. It Is Not The Intent Of This Specification To Define The Internal Arc May 16th, 2022.

ROADRUNNER EXPRESS Page 4 ROADRUNNER EXPRESS ROADRUNNER EXPRESS
Page 4 MEET OUR ROADRUNNER ATHLETES Roadrunners Volleyball A And Teams Coached By Mr. Sam Obina And His Wife, Tess. Kneeling From Left To Right: Sternal Edipo, Ras-g Andon, Micah I Sep 11th, 2022

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