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Data Bus, Address Bus, And Control Bus. The CPU Leaves The Control Over Bus And  
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State And The DMA Controller Has To Manage The Operat Jan 20th,  
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Flags To Test Data Conditions. For Example, After An Addition Of Two Numbers, If  
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Indicate A Carry --called Mar 5th, 2024.  
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& Sensor Interfacing-External Memory Interfacing- Stepper ... Marilyn Wolf  
"Computer As Components-Principles Of Embedded Computing System Design",  
Third Edition, Morgan Kaufmann Publisher, 2012. 2. Yn-cheng Liu, Glenn A.G Mar  
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Microprocessor And Microcontroller - Weebly  
Microprocessor Trainer Kit. EQUIPMENT REQUIRED Intel 8085 Based MPS - 85 - 2 Trainer Kit, DAC Interface  
Card, 26 Pin Flat Cable Connector, CRO With Probe. ALGORITHM:- SQUARE WAVE  
FORM 1. Initialize The Control Word Register I.e., 8255 As Input Port. 2. Send 00 To  
Port A Of 8255 3. Call Delay Subroutine 4. Send FF To Port A Of 8255 5. Apr 22th,  
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2. Mohamed Ali Mazidi, Janice Gillis Mar 15th, 2024  
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Intel Microprocessor Microcontroller Lab Manual  
DIVISION OF TWO 16-BIT DATA AIM: To Multiply Two 16-bit Data (Multibyte Division) Using 8086 Microprocessor.

APPARATUS: 1. 8086 Trainer Kit 2. Key Board 3. SMPS PROGRAM CODE: MOV

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