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1.6.5 Advantages Of Flowchart 1.6.6 Limitations Of Flowchart 1.7 Pseudocode Example 1.8 Divide And Conquer Strategy 1.8.1 Merge Sort 1.9 Writing Algorithms And Flow Charts For Simple Exercises 1.10 Let Us Sum Up 1.11 Lesson And Activity 1.12 Keywords 1.13 Questions For Discussion Jan 25th, 2024

BCA - PGDCA SEM-2 (NEW Course) BCA/DCA 201 Advanced ...

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BCA Revised Syllabus (w.e.f. 2019-20 And Onwards) Subject Code Subject Title
Teaching Scheme Hrs/week Examination Exam. Duration (Hrs) Marks Theory
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To Implement A Half Adder Using Basic Gates To Implement A Half Subtractor Using
Universal Gate (NAND) Equipment The Equipment You Require Is As Follows: IC

7404, IC 7400, IC 7483 A Half Adder Is A Logical Circuit That Performs An Addition Operation On Two One-bit Binary Numbers Often Written As A And B. Mar 6th, 2024

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Basic Computer Organisation And Design, Programming The Computer With Assembly Language (same Basic Applications), Micro-programmed Control. Books:

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2. Prepare WR By Mixing 50 Parts Of BCA Reagent A With 1 Part Of BCA Reagent B (50:1, Reagent A:B). For The Above Example, Combine 50 MI Of Reagent A With 1 MI Of Reagent B. Note: When Reagent B Is First Added To Reagent A, A Turbidity Is Observed That Quickly Disappears Upon Mixing To Yield A Clear, Green WR. Jan 26th, 2024

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Question Bank - MCQs BCA-203 Object-oriented Programming Using C++ 1. UNIT - 1 (OOP Concepts And Introduction To C++) 1 In Structured Programming, The Problem Is Divided Into Various _____. Apr 4th, 2024

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