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The Input Module Is Designed With 256x Oversampling Delta-sigma ADC Technology, Internal Filtering Ensures That SNRs Of 86dB Are Readily Achieved. Provision Has Been Made For An External Over-sampling Clock Via The Front Connector, And An Internal Sample Clock, Which Is User Selectable. The Internal Sampling Clock Utilizes A Frequency Mar 9th, 2024

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2 Discrete Modulation Of Analog Signals In Order To Transmit A Signal It Is Necessary To Encode The Digital Signal (bits) On The "natural" Signals For The Medium Over Which The Transmission Is To Take Place. Recall That A Signal Has Three Components: S(t) = A(t)sin(2...ft+`), Where The Parameters Jan 7th, 2024

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